## **FACULTY PROFILE**

Name: RAMPRASAD RAVULA

Designation: Sr. Asst. Professor

Qualification: M. Tech (Ph. D)

Date of birth: 11.02.1983

**Experience: 15.6 Years** 

Teaching: 14Y Industrial: Nil Research: 1Y 6M

Area of Specialization: VLSI System Design

**Date of joining: 05.05.2023** 

**Research Interests: Antennas** 

Address for Communication: H NO:3-785

SAI NAGAR COLONY

PARKAL POST WARANGAL DIST

**TELANGANA - 506164** 

Email: ramprasad.ece@srivasaviengg.ac.in

Mobile No: 9491020333

Academic Degree: (From highest)

Degree	School/College	Board/University	Year of Pass
Ph. D	Puducherry Technological University	Puducherry Technological University	Pursuing
M. Tech	JNTU Kakinada	JNTU Kakinada	2009
B. Tech	Sai Spurthi Institute of Technology	JNTU Hyderabad	2005
Inter	Sri Kaveri Junior College	Board of Intermediate Education	2001
10 <sup>th</sup>	Sri Saraswathi Shishu Mandir	Board of Secondary Education	1999

**Experience particulars: (latest first)** 

College/University	Designation	Duration
Puducherry	Research Scholar	27.09.2021 to
Technological University		28.04.2023



Sai Spurthi Institute of	Associate Professor	01.01.2010 to
Technology		25.09.2021
Sai Spurthi Institute of	Assistant Professor	10.11.2005 to
Technology		30.06.2007

## **Administration:**

Papers Published in referred Journals: 05

National: Nil International: 05

Papers presented in Conferences: 05

National: 03 International: 02

Keynote address / Guest Lectures delivered: 04

No. of Workshops / Seminars/FDPs Conducted: 02

No. of Workshops / Seminars/FDPs Participated: 10

No. of projects guided/guiding:

UG: 12 PG: Nil

No. of Ph.D.'s guided/guiding: Nil

Memberships in Professional Societies: ISTE (Life Membership - LM - 59340)

Achievements: (If any)